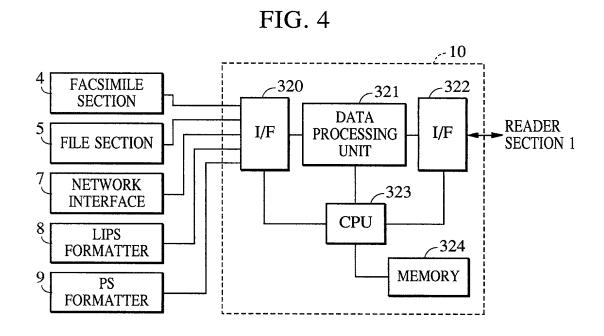
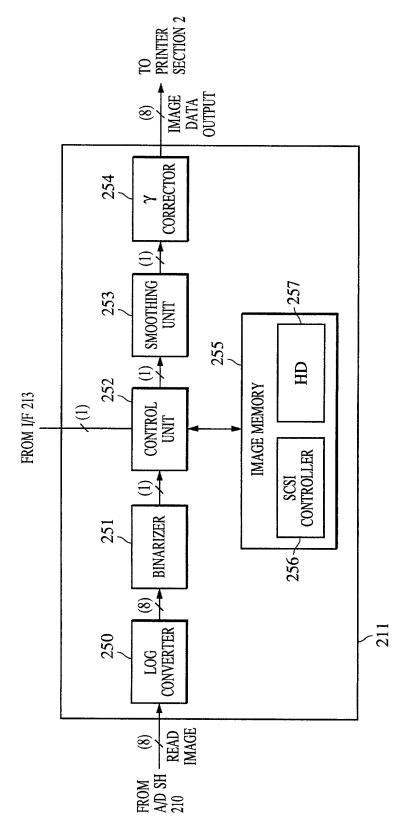


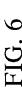
FIG. 2

FIG. 3 -209 -210-211A/D **IMAGE** PRINTER **CCD SECTION 2** SH **PROCESSOR** <u>_</u>216 -213 214~ **CPU** I/F **CORE MEMORY** SECTION 10 CONTROL 500 **PANEL**









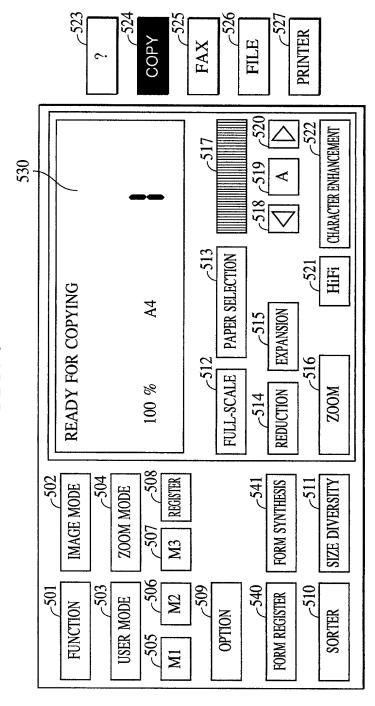


FIG. 7

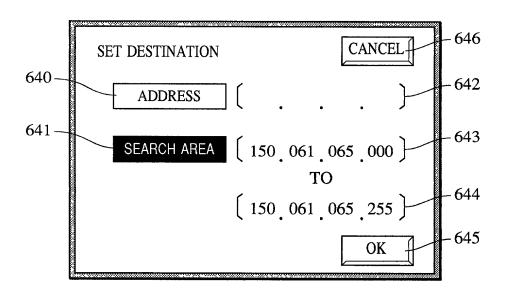
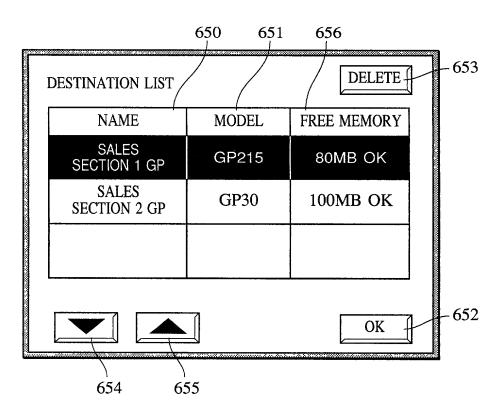
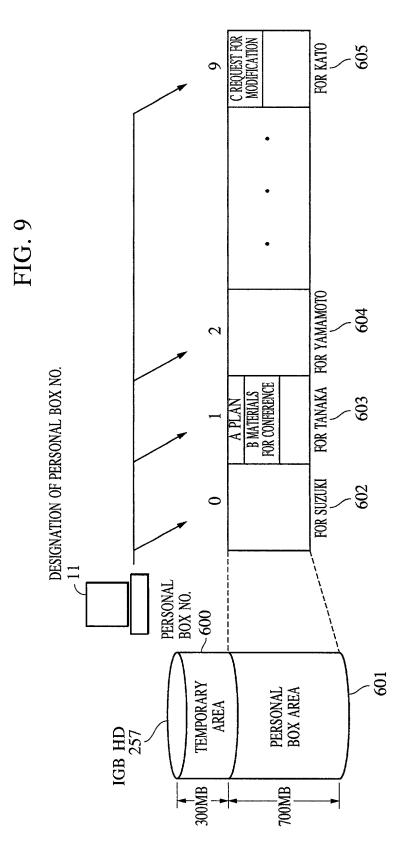


FIG. 8





For the first state of the first

FIG. 10

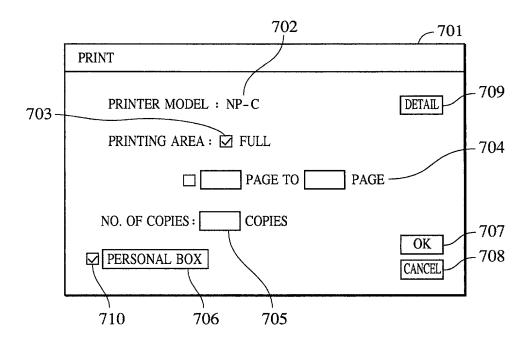
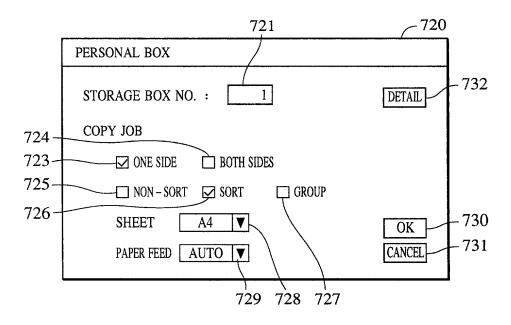


FIG. 11



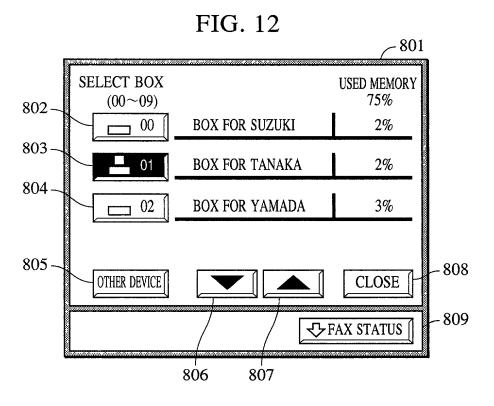
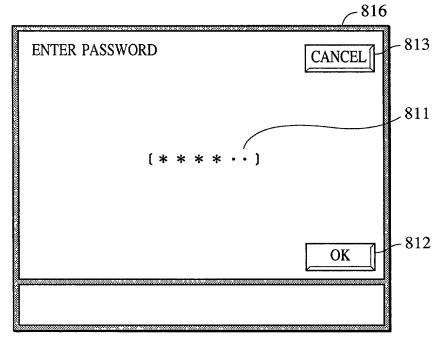


FIG. 13



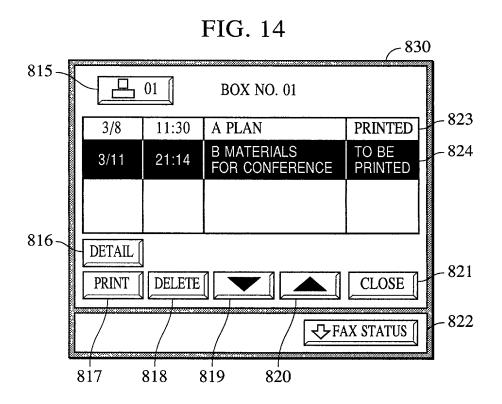


FIG. 15

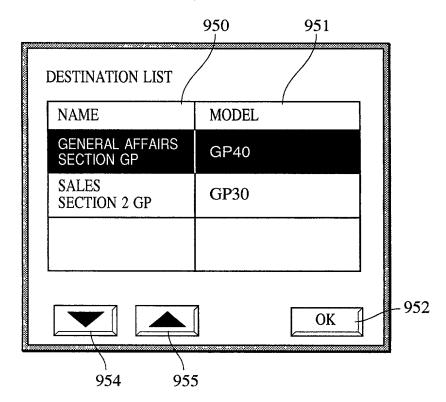
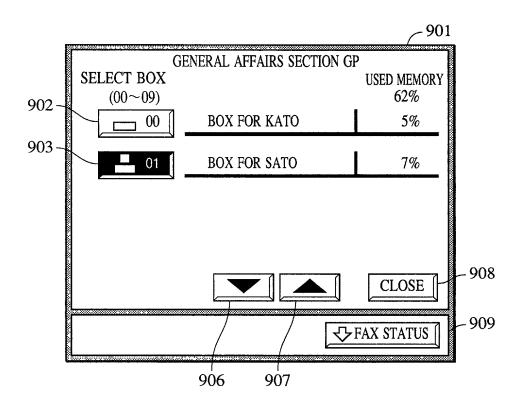


FIG. 16



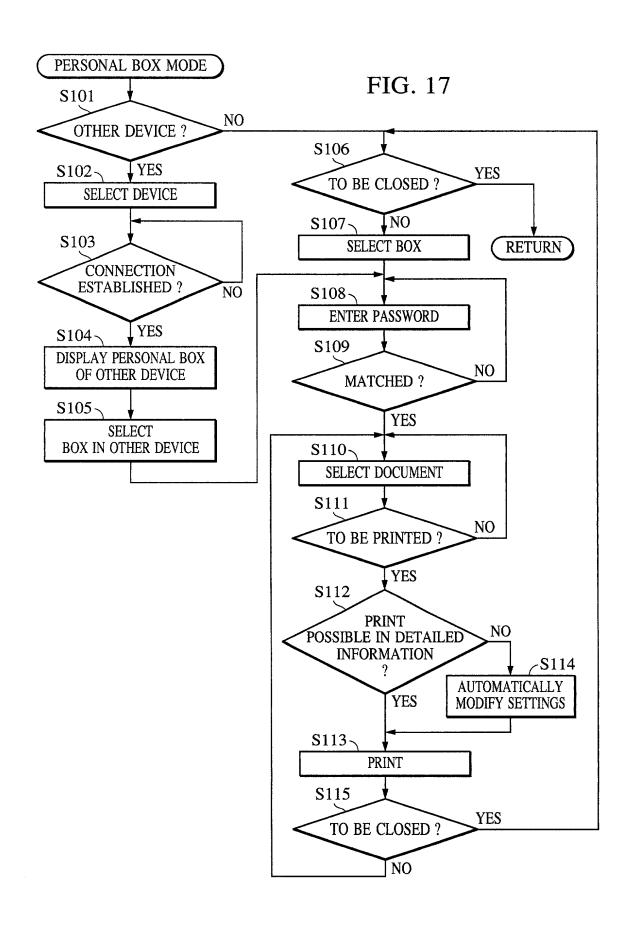


FIG. 18

